

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hoppensteadt et al. : Group Art Unit: 2121
Serial No.: 09/771,019 : Examiner: Hirl, Joseph P.
Filed: January 26, 2001 :
Title: **PHASE-LOCKED LOOP OSCILLATORY NEUROCOMPUTER**



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Sir:

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Respectfully submitted,

GALLAGHER & KENNEDY, P.A.

Date: 7/21/04

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Complete if Known

Application Number	09/771,019
Filing Date	January 26, 2001
First Named Inventor	Hoppensteadt et al.
Art Unit	2121
Examiner Name	Hirl, Joseph P.
Attorney Docket Number	9138-0023US

Sheet 1 of 1

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Technology Center 2100**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	1.	F. C. HOPPENSTEADT, E. IZHIKEVICH, "Canonical Models for Bifurcations from Equilibrium in Weakly Connected Neural Networks," WCNN'95, Washington, D.C., Vol. 1, pp. 180-183.	
	2.	F. C. HOPPENSTEADT, E. M. IZHIKEVICH, "Synaptic Organizations and Dynamical Properties of Weakly Connected Neural Oscillators," Biol. Cybern. 75, 117-127 (1996).	
	3.	F. C. HOPPENSTEADT, E. M. IZHIKEVICH, "Synaptic Organizations and Dynamical Properties of Weakly Connected Neural Oscillators," Biol. Cybern. 75, 129-135 (1996).	
	4.	E. Ahissar, "Temporal-Code to Rate-Code Conversion by Neuronal Phase-Locked Loops," Neural Computation 10, 597-650 (1998).	

Examiner Signature	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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